

<b>Notice of References Cited</b>	Application/Control No. 10/732,963	Applicant(s)/Patent Under Reexamination KNORR ET AL.	
	Examiner Leonardo Andújar	Art Unit 2826	Page 1 of 1

**U.S. PATENT DOCUMENTS**

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*	B	US-6,444,557 B1	09-2002	Biolsi et al.	438/597
*	C	US-6,593,220 B1	07-2003	Yu et al.	438/612
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 2000, Volume I, Lattice Press, 719-727, 791-795.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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